

Notice of References Cited	Application/Control No. 09/873,580	Applicant(s)/Patent Under Reexamination FUJITA, HARUMITSU	
	Examiner Fernando Toledo	Art Unit 2823	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
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	B	US-6294430-B1	09-2001	Fatow et al.	438	264
	C	US-				
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	M	US-				

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era Volume 2: Process Integration; pp. 354 - 361; Lattice Press 1990
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.